Abstracts

A 5.8 GHz OFDM GaAs MESFET MMIC chip set

S. Yoo, D. Heo, C.-H. Lee, B. Matinpour, S. Chakraborty and J. Laskar. "A 5.8 GHz OFDM GaAs MESFET MMIC chip set." 2000 MTT-S International Microwave Symposium Digest 00.3 (2000 Vol. III [MWSYM]): 1273-1276.

This paper presents the implementation of a 5.8 GHz GaAs MESFET MMIC transceiver chip set compatible with the OFDM standard. The receiver is designed for low noise figure and high IIP3. The transmitter is designed to satisfy high peak-to-average power ratio. This design matches closely with the requirements for 5.8 GHz wireless LAN applications. To the best of our knowledge, this research represents the first reported implementation of the OFDM standard at 5.8 GHz.

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